Research paper

ISSN PRINT 2319 1775 Online 2320 7876

© 2012 LJFANS. All Rights Reserved, UGC CARE Listed (Group -I) Journal Volume 10, Iss 1, 2021

A PV System with a Flying Capacitor Multilevel Topology with APOD and POD Pulse Width Modulation

Dr Sk.Moulali

Department of Electrical and Electronics Engineering, Koneru Lakshmaiah Education Foundation. Vaddeswaram, Guntur, Andhra Pradesh, India.

moulali@kluniversity.in

Abstract

This study of a three- and five-level flying capacitor multilevel inverter for a PV system uses phase disposition PWM and phase opposition phase disposition modulation approaches. The control approach of three and five level flying capacitor multilevel inverters uses the multicarrier PWM (MCPWM) techniques phase opposition disposition method. The three and five flying capacitor multilevel inverter is controlled in this study using the in phase disposition PWM controlling approach. MATLAB (2017) was used for the simulation, and the output voltage waveforms are recorded. Comparing the multilayer inverter to the cascaded H-bridge multilevel inverter, it is discovered that the multilevel inverter has less Total Harmonic Distortion (THD).

INTRODUCTION

The introduction of Multilevel inverters and the recent rapid growth of this new field have transformed power electronics [1]. There are several applications for this technology, including in industries [2]. Multilevel inverter technology is a rapidly evolving field that advances on a daily basis [3]. New iterations and variations of multilevel inverter technology have been noted frequently in recent literature. This paper's primary objectives are a review and an in-depth analysis of the rapidly evolving power electronic technology [4].



Flying capacitor multilevel inverter (a) three-level, (b) five-level.

APODPWM, (N+1) modulation strategy:; N=4 a) carriers disposi- tion b) Simple output voltage: 5 voltage levels



Upper and lower arms voltage levels a) PODPWMb)PDPWM



 ISSN PRINT 2319 1775 Online 2320 7876

 Research paper
 © 2012 IJFANS. All Rights Reserved, UGC CARE Listed (Group -1) Journal Volume 10, Iss 1, 202

3-SM MMC converter leg characteristics a) Number of ON switch-ed SMs b) Inductor voltage c) circulating current



CONCLUSION

In this study, a 3-stage, 5-level fell is recreated.

With the help of the APOD and POD control approaches, a multi-

level inverter has been put into practice in a simulation environment. The THD has been demo nstrated by many Modulation indicators. We may conclude from the THD analysis that POD t echniques have lower THD when compared to APOD. The THD increases for modulating ind ices from 0 to 1 and then decreases for both techniques after that. As a result, it can be inferre d from the aforementioned simulation and its accompanying outcomes that POD will be the s uperior method over APOD.

REFERENCES

- [1] J. Rodriguez, J.-S. Lai, and F.Z. Peng, "Multilevel inverters: A sur- vey of topologies, controls, and applications," IEEE Trans. Ind. Electron., vol. 49, no. 4, pp. 724–738, Aug. 2002.
- [2] L. Franquelo, J. Rodriguez, J. Leon, S. Kouro, R. Portillo, and M. Prats, "The Age of Multilevel Converters Arrives," IEEE Ind. Elec-tron. Mag., vol. 2, no. 2, pp. 28-39, June 2008.
- [3] J. Rodriguez, L. Franquelo, S. Kouro, J. Leon, R. Portillo, M. Prats, and M. Perez, "Multilevel Converters: An Enabling Technology for High-Power Applications," Proc. IEEE, pp. 1786-1817, vol. 97, no. 11, Nov. 2009.
- [4] D. Soto and T. Green, "A comparison of high-power converter to- pologies for the implementation of FACTS controllers," IEEE Trans. Ind. Electron., vol. 49, no. 5, pp. 1072– 1080, Oct. 2002.